

FIG. 1B

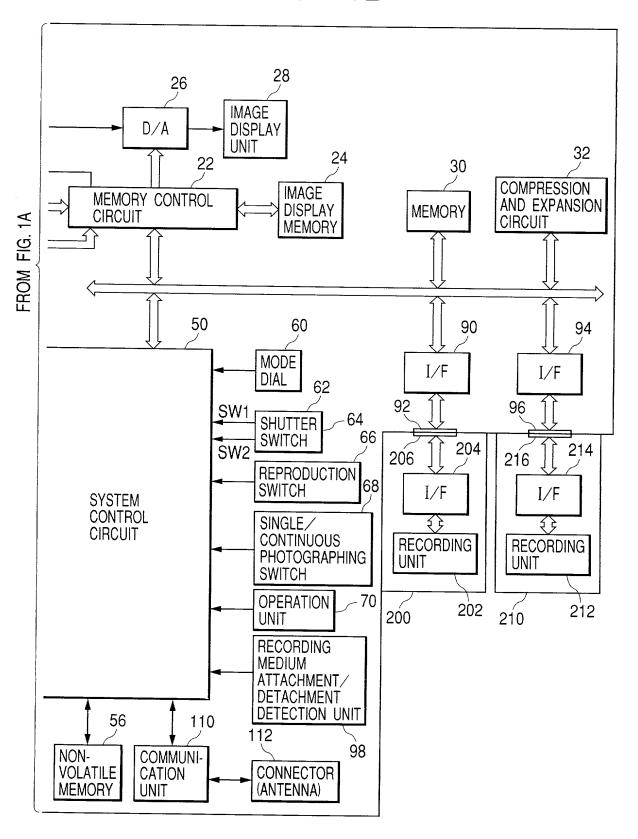


FIG. 2

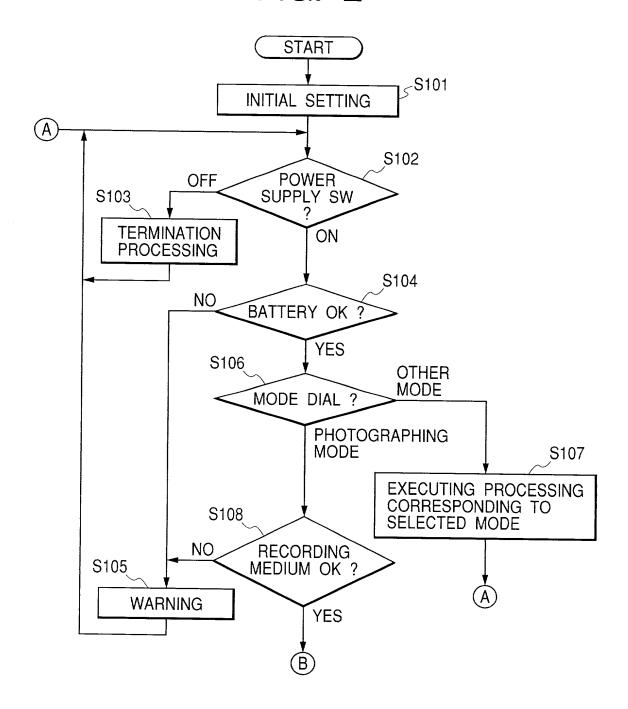


FIG. 3

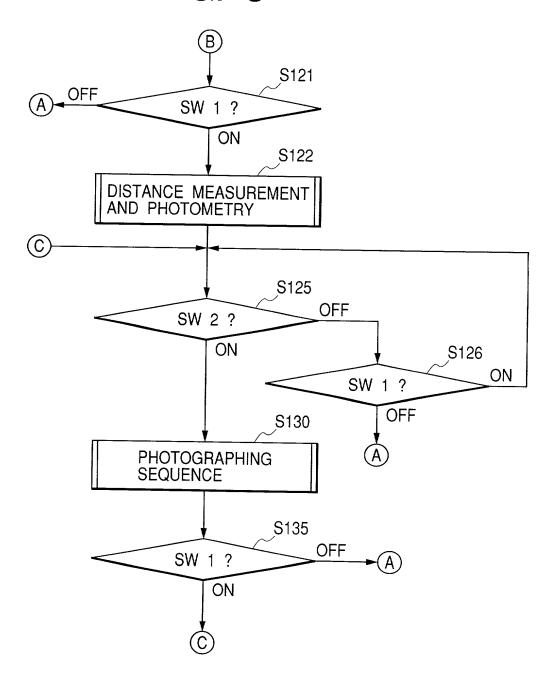
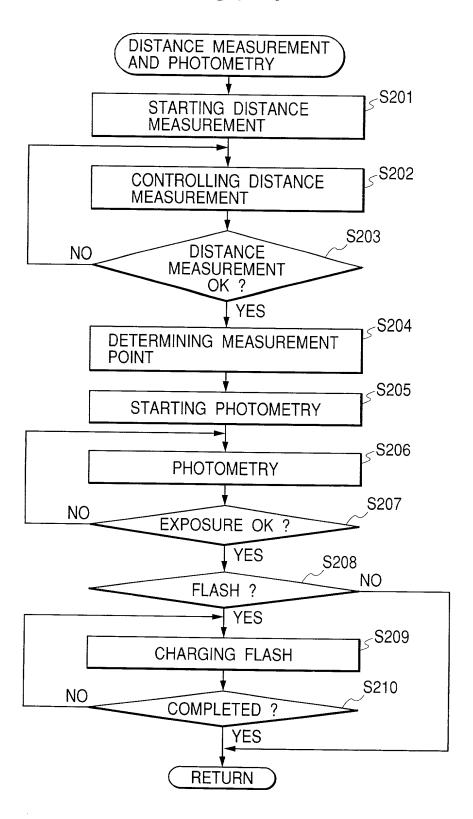
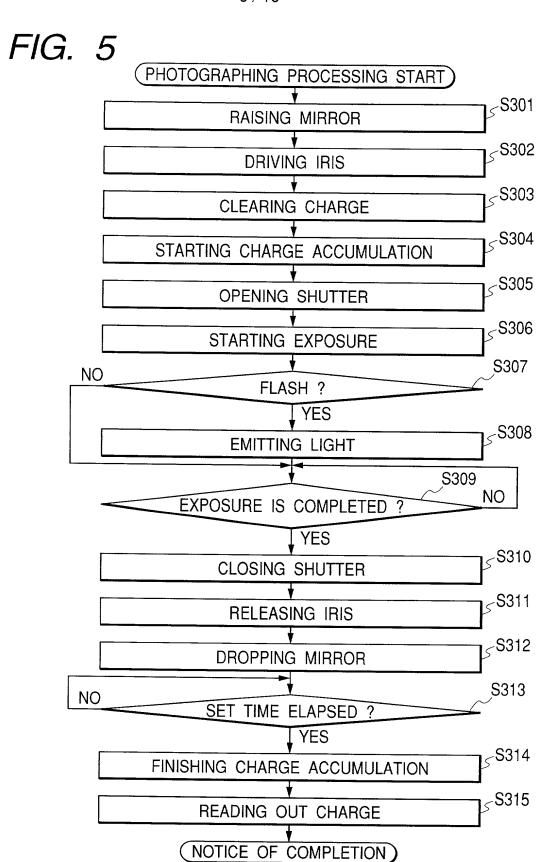


FIG. 4





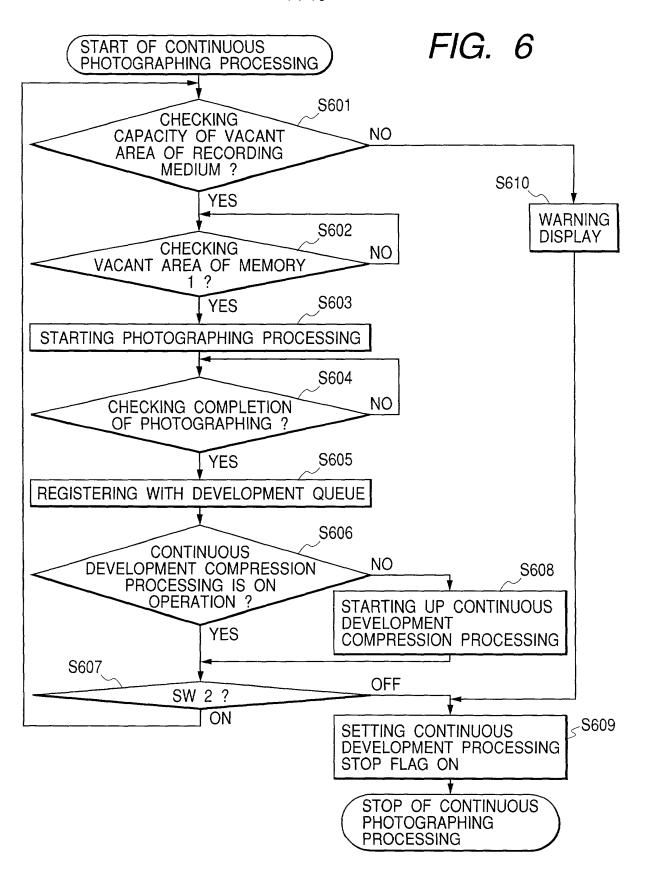


FIG. 7

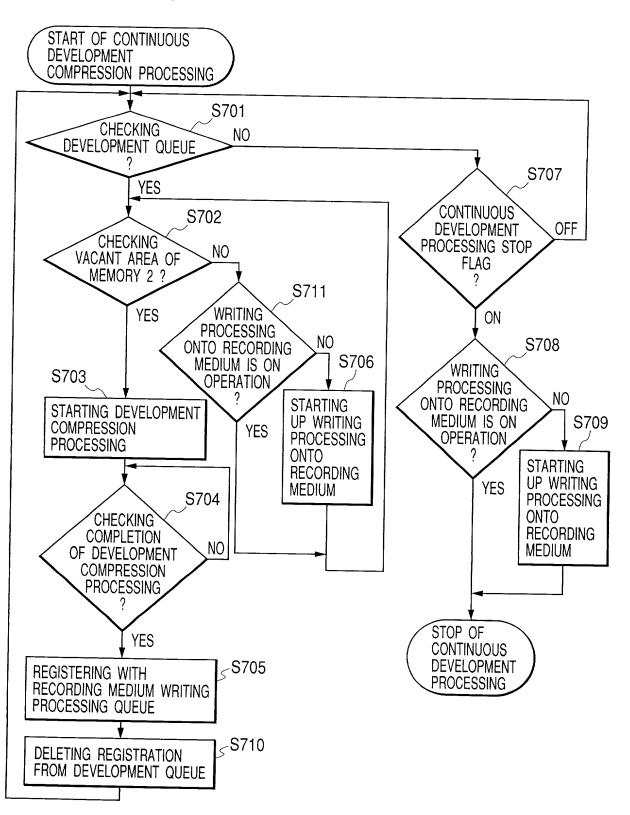


FIG. 8

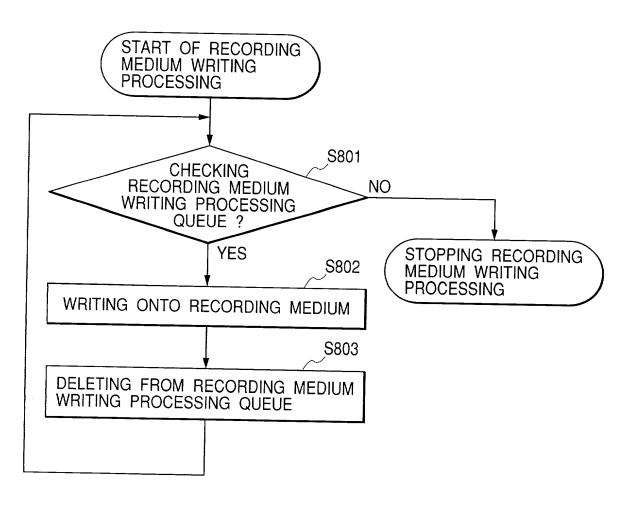
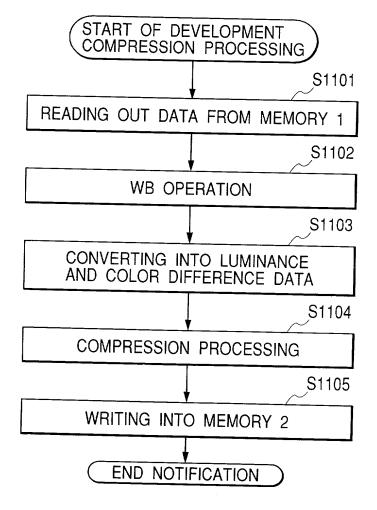


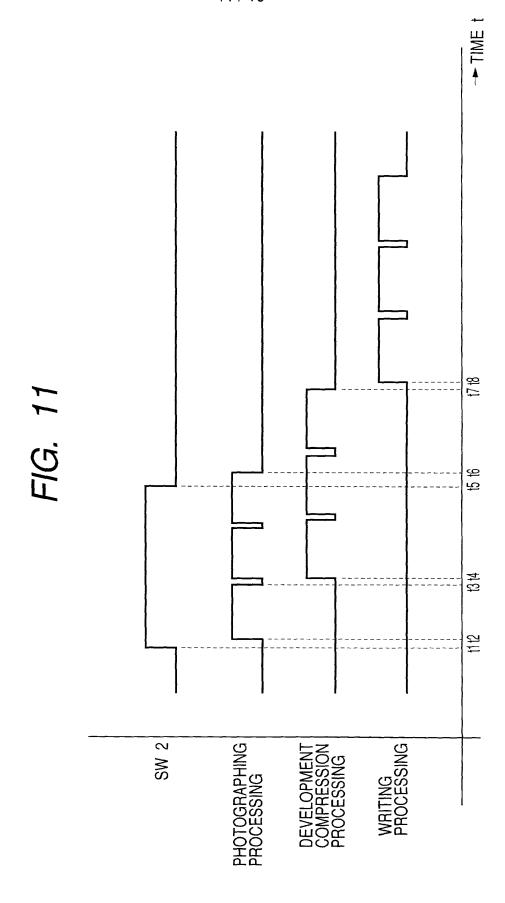
FIG. 9

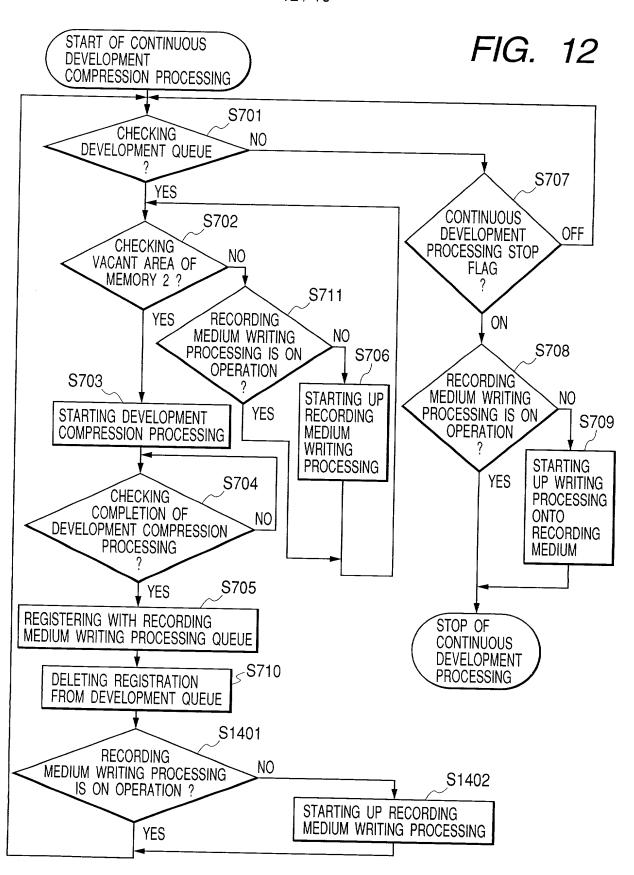
MEMORY 30

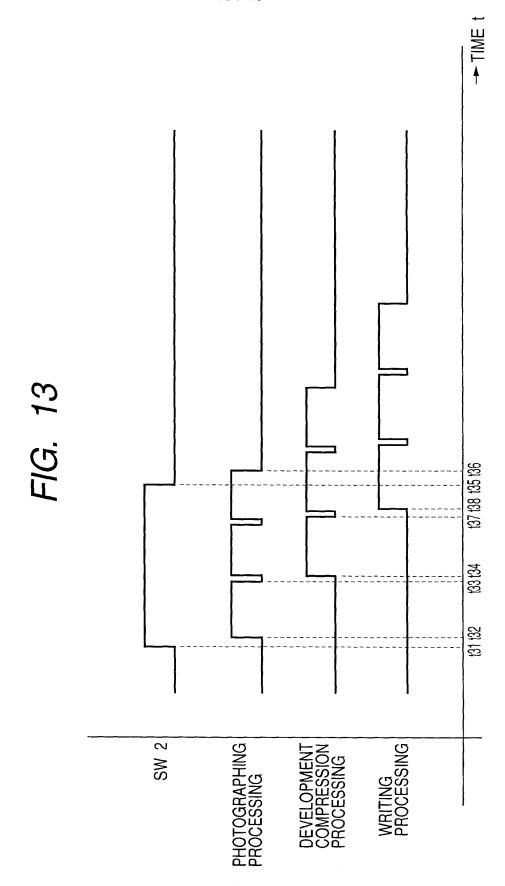
MEMORY AREA M1 MEMORY AREA M2 MEMORY AREA M3

FIG. 10









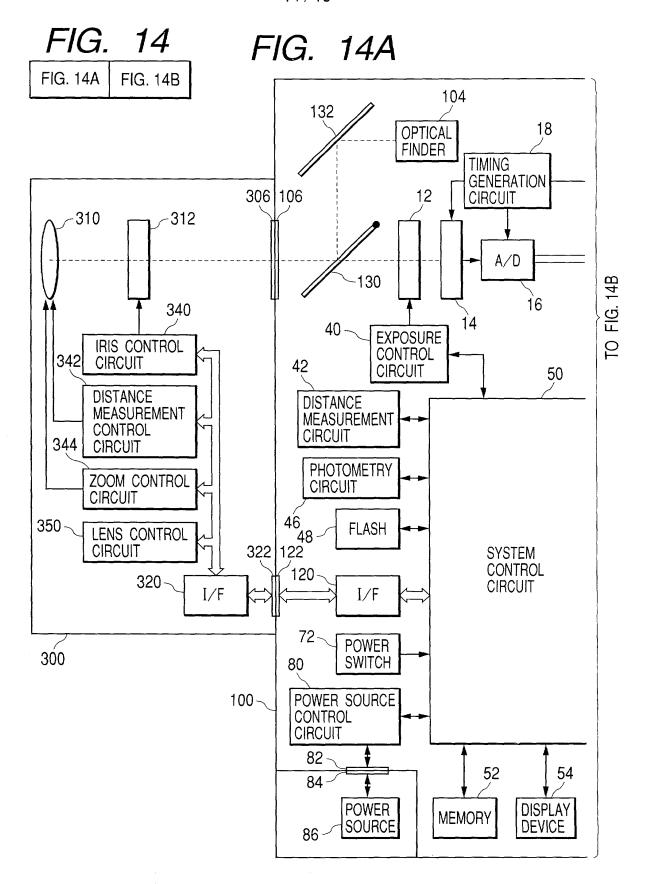


FIG. 14B

